

Abstracts

A 40 Gbit/s super-dynamic decision IC using 0.15- μ m GaAs MESFETs

K. Murata, T. Otsuji, M. Yoneyama and M. Tokumitsu. "A 40 Gbit/s super-dynamic decision IC using 0.15- μ m GaAs MESFETs." 1997 MTT-S International Microwave Symposium Digest 2. (1997 Vol. II [MWSYM]): 465-468.

This paper describes a 40 Gbit/s decision IC using 0.15- μ m gate length GaAs MESFETs. A super-dynamic flip-flop circuit and wideband amplifier were applied in order to attain 40 Gbit/s operation. The IC is the first to provide an error-free 40 Gbit/s decision operation.

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